

In Place of FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANTS' INFORMATION DISCLOSURE
STATEMENT**

Serial No.:
Applicants: Victor R. Augsburg et al.
Filing Date: (herewith)
Group:
Atty. Docket No.: RPS920010178US1

Jc971 U.S. PRO
10/082005
02/25/02

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
B50 AAA	5,619,408	04/08/97	Black et al.	395	567	—
B50 ABC	5,717,587	02/10/98	Black et al.	364	131	—
B50 ACA	5,819,059	10/06/98	Tran	395	389	—
B50 ADA	5,940,602	08/17/99	Narayan et al.	395	389	—
B50 AEA	4,649,478	03/10/87	Worley, Jr.	364	200	—
B50 AFA	5,233,696	08/03/93	Suzuki	395	375	—
B50 AGA	5,337,415	08/09/94	DeLano et al.	395	375	—
B50 AHA	5,651,122	07/22/97	Hartung et al.	395	384	—
B50 AIA	5,852,741	12/22/98	Jacobs et al.	395	800.24	—
B50 AJA	5,970,235	10/19/99	Witt et al.	395	389	—
B50 AKA	6,263,429 B1	07/17/01	Siska	712	245	—
ALA						
AMA						
ANA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes <u> </u> No <u> </u>
AOA						
APA						
AQA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

B50
7/23/04

B50 ARA J. A. Kahle, "Opcode Remap and Compression in Hard-Wired RISC Microprocessor," *IBM Technical Disclosure Bulletin*, Vol. 32, No. 10A, March 1990, p. 349.

B50 ASA V. R. Augsburg et al., Pending Patent Application docket No. RPS920010176US1 entitled "Efficiently Calculating a Branch Target Address" Serial No. 10/082,144 filed on 2/25/02

Examiner:

Date Considered:

7/23/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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